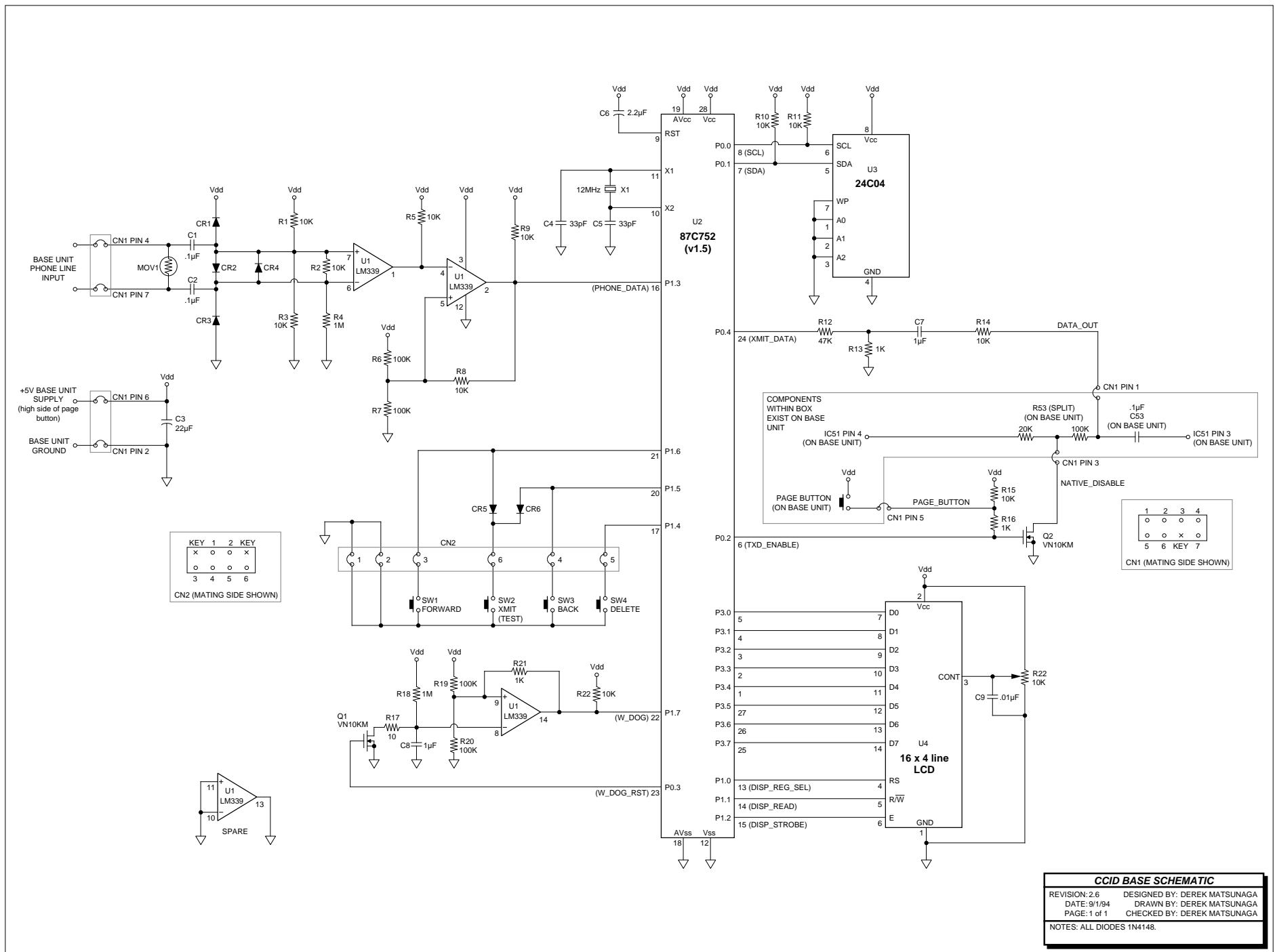


CCID SYSTEM BLOCK DIAGRAM	
REVISION: 1.0	DESIGNED BY: DEREK MATSUNAGA
DATE: 9/28/94	DRAWN BY: DEREK MATSUNAGA
PAGE: 1 of 1	CHECKED BY: DEREK MATSUNAGA
NOTES: NONE	



CCID BASE SCHEMATIC
 REVISION: 2.6 DESIGNED BY: DEREK MATSUNAGA
 DATE: 9/1/94 DRAWN BY: DEREK MATSUNAGA
 PAGE: 1 of 1 CHECKED BY: DEREK MATSUNAGA
 NOTES: ALL DIODES 1N4148.

